



# ISSCC 2026 Awards

February 2026





## 2025 Lewis Winner Award for Outstanding Paper

**“A 9.05-to-37.0GHz LO Generator with Magnetic Mode Switching and Tuning-Free Octave-Bandwidth Common-Mode Resonator Achieving >190.7dBc/Hz FoM”**



**Hao Guo**



**Yaolong Hu**



**Taiyun Chi**

Rice University, Houston, TX

**The Lewis Winner Award for Outstanding Paper**

**Lewis Winner** served ISSCC for three decades (1958 to 1988) as Editor, Publisher, and Conference Coordinator. He was responsible, in considerable part, for establishing and maintaining the high standards of ISSCC publications and presentations. The Award in his honour is based on ratings by Conference attendees in the previous year.

## 2025 Anantha P. Chandrakasan Award for Distinguished-Technical-Paper

**“A 212.5Gb/s DSP-Based PAM-4 Transceiver with 50dB Loss Compensation for Large AI System Interconnects in 4nm FinFET”**



**E-Hung Chen<sup>1</sup>**



**Henry Park<sup>2</sup>**



**Mohammed Abdullatif<sup>2</sup>**



**Miguel F. Gandara<sup>2</sup>**



**Ahmed ElShater<sup>2</sup>**



**Amr Khashaba<sup>2</sup>**



**Shih-Hao Huang<sup>1</sup>**



**Tsz-Bin Liu<sup>1</sup>**



**Atharav Atharav<sup>2</sup>**



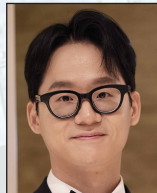
**Joonyeong Lee<sup>2</sup>**



**Qaiser Nehal<sup>2</sup>**



**Mohamed Megahed<sup>2</sup>**



**Yusang Chun<sup>2</sup>**



**Cheng-En Shieh<sup>1</sup>**



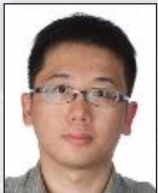
**Vidhan Jolly<sup>2</sup>**



**SoonWon Kwon<sup>2</sup>**



**Hsin-Ta Chien<sup>1</sup>**



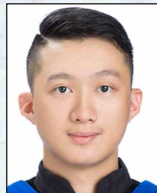
**Ke-Chung Wu<sup>1</sup>**



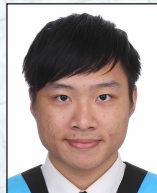
**Cheng-En Liu<sup>1</sup>**



**Peng Yan<sup>2</sup>**



**Po-Jui Li<sup>1</sup>**



**Chun-Han Chen<sup>1</sup>**



**Tzu-Shun Lin<sup>1</sup>**



**Pei-Chieh Liu<sup>1</sup>**



**Tamer Ali<sup>2</sup>**

<sup>1</sup>MediaTek, Hsinchu, Taiwan

<sup>2</sup>MediaTek, Irvine, CA

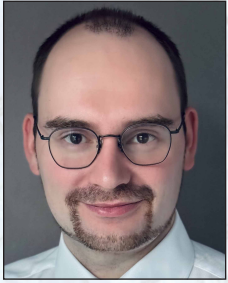
\*Equally Credited Authors (ECAs)

**Anantha P. Chandrakasan Award for Distinguished-Technical Paper**

This Award is based on ITPC ratings at paper selection, as normalized at paper presentation time by members of the Awards & Recognition Committee (ARC). In recognition of the many decades of contribution by Anantha Chandrakasan, and his retirement as Conference Chair, this Award was renamed in his honour in 2019.

## 2025 Jan Van Vessel Award for Outstanding EWAA Paper

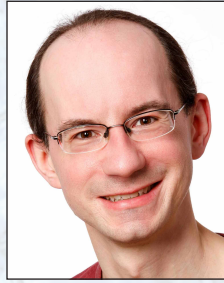
“A Cryo-BiCMOS Controller for  ${}^9\text{Be}^+$ -Trapped-Ion-Based Quantum Computers”



Peter Toth<sup>1</sup>



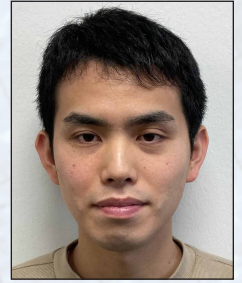
Paul Shine Eugene<sup>1</sup>



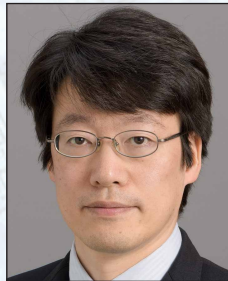
Sebastian Halama<sup>2</sup>



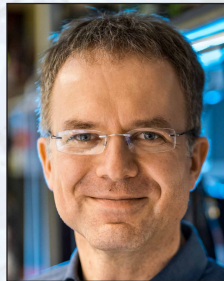
Yerzhan Kudabay<sup>1</sup>



Kaoru Yamashita<sup>1,3</sup>



Hiroki Ishikuro<sup>3</sup>



Christian Ospelkaus<sup>2</sup>



Vadim Issakov<sup>1</sup>

<sup>1</sup>Technische Universität Braunschweig, Braunschweig, Germany

<sup>2</sup>Leibniz University Hannover, Hannover, Germany

<sup>3</sup>Keio University, Yokohama, Japan

The Jan Van Vessel Award for Outstanding EWAA (Europe, West Asia, Africa) Paper  
Jan Van Vessel served ISSCC as the first Chair of the European Program Committee from 1968 to 1974.  
The Award in his honour is based on ratings by Conference attendees in the previous year.

## 2025 Takuo Sugano Award for Outstanding APAC Paper

“An 850 $\mu\text{W}$  2-to-5GHz Jitter-Filtering and Instant-Toggling Injection-Locked Quadrature-Clock Generator for Low-Power Clock Distribution in HBM Interfaces”



Jeongbeom Seo<sup>1</sup>



Yoonseo Cho<sup>1,2</sup>



Yuhwan Shin<sup>1,2</sup>



Jaehyouk Choi<sup>1</sup>

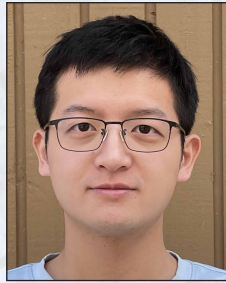
<sup>1</sup>Seoul National University, Seoul, Korea

<sup>2</sup>KAIST, Daejeon, Korea

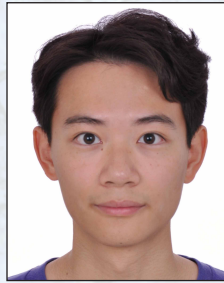
The Takuo Sugano Award for Outstanding APAC (Asia Pacific) Paper  
Takuo Sugano served ISSCC as an early member of what became the Far-East Program Committee, from 1967 to 1989.  
The Award in his honour is based on ratings by Conference attendees in the previous year.

## 2025 Jack Kilby Award for Outstanding Student Paper

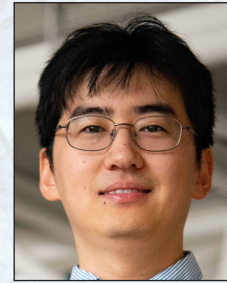
“A Blocker-Tolerant mm-Wave Low-Noise Amplifier Utilizing Doherty Active Load Modulation for Linearity Enhancement”



Hao Yu



Lianbo Liu



Sensen Li

University of Texas, Austin, TX

### The Jack Kilby Award for Outstanding Student Paper

Jack Kilby, an integrated-circuits pioneer responsible for designing the first monolithic IC, was a long-term ISSCC participant and contributor. The Award in his honour is based on ratings by Conference attendees in the previous year.

## 2025 Jack Kilby Award for Outstanding Student Paper

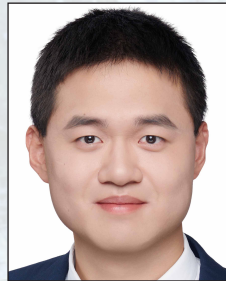
“A 70dB SNDR 80MHz BW Filter-Embedded Pipeline-SAR ADC Achieving 172dB FoMS with Progressive Conversion and Floating-Charge-Transfer Amplifier”



Siyu Huang



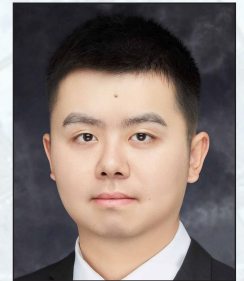
Zhishuai Zhang



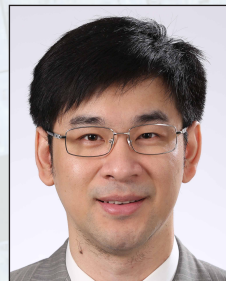
Xiyu He



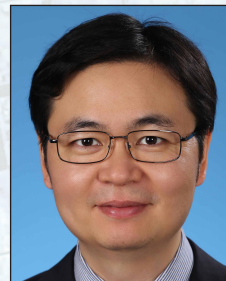
Mingyang Gu



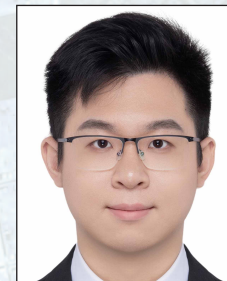
Yunsong Tao



Yi Zhong



Nan Sun



Lu Jie

Tsinghua University, Beijing, China

### The Jack Kilby Award for Outstanding Student Paper

Jack Kilby, an integrated-circuits pioneer responsible for designing the first monolithic IC, was a long-term ISSCC participant and contributor. The Award in his honour is based on ratings by Conference attendees in the previous year.

## 2025 ISSCC Award for Outstanding Forum Presenter

“Dataflow Optimization and Data Sparsity Management for ML Accelerators”



**Marian Verhelst**  
KU Leuven, Heverlee, Belgium

**The Outstanding Forum-Presenter Award**  
This Award is based on Forum-attendee ratings from the previous year.

## 2025 ISSCC Award for Outstanding Forum Presenter

“Innovation Trends in Depth Sensing and Imaging: Enabling Technologies and Core Building Blocks”

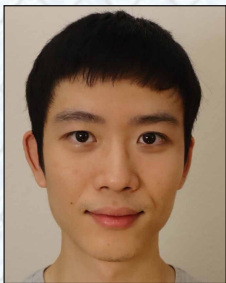


**David Stoppa**  
Sony Europe, Trento, Italy

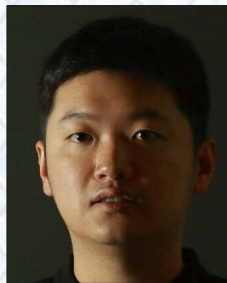
**The Outstanding Forum-Presenter Award**  
This Award is based on Forum-attendee ratings from the previous year.

## 2025 Demonstration Session Award

“An 8.62 $\mu$ W 75dB-DRSoC End-to-End Spoken-Language-Understanding SoC with Channel-Level AGC and Temporal-Sparsity-Aware Streaming-Mode RNN”



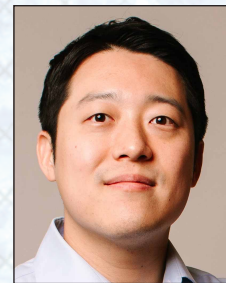
**Sheng Zhou<sup>1</sup>**



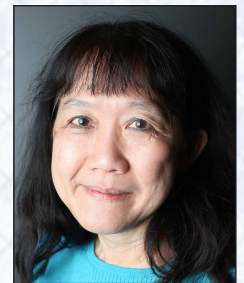
**Zixiao Li<sup>1</sup>**



**Tobi Delbruck<sup>1</sup>**



**Kwantae Kim<sup>2</sup>**



**Shih-Chii Liu<sup>1</sup>**

<sup>1</sup>University of Zurich and ETH Zurich, Zurich, Switzerland  
<sup>2</sup>Aalto University, Espoo, Finland

**Demonstration-Session Award**  
This Award is based on ratings provided by the Demo-Session Committee from the previous year.

## 2025 Demonstration Session Award

“A 2.2pJ/b 212.5Gb/s PAM-4 Transceiver with >46dB Reach in 5nm FinFET”



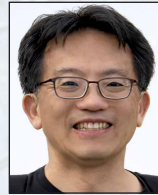
Ahmed Mostafa<sup>1</sup>



Amr Hassan<sup>1</sup>



Amit Singh<sup>3</sup>



Cho-Ru Yang<sup>1</sup>



Deepak Prabakaran<sup>3</sup>



Dadian Zhou<sup>1</sup>



Edmund Hsiao<sup>1</sup>



Fangqing Chu<sup>1</sup>



Fyyaz Aziz Khan<sup>1</sup>



Fei Lu<sup>1</sup>



Guoping Cui<sup>1</sup>



Joseph Ntonio<sup>5</sup>



Jingjing Deng<sup>1</sup>



Jackson Ding  
Tek Kon<sup>1</sup>



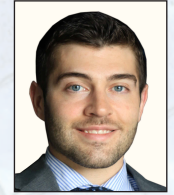
Jianmin Guo<sup>1</sup>



Lidan Jiang<sup>1</sup>



Kuan Ming Lu<sup>4</sup>



Matthew Kelly<sup>6</sup>



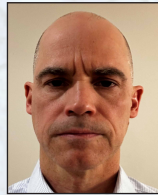
Manisha  
Gambhir<sup>1</sup>



Mallikahrjuna  
Rao Patoju<sup>3</sup>



Mahendra Pal Singh<sup>1</sup>



Michael  
Shannon<sup>5</sup>



Peng Liu<sup>1</sup>



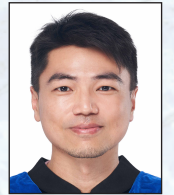
Purushotham  
Ramakrishna<sup>3</sup>



Chiahung Chen<sup>4</sup>



Sina Naderi Shahi<sup>8</sup>



Yi-Pei Su<sup>4</sup>



Zubir Adai<sup>1</sup>



Zhipeng Li<sup>1</sup>



Dishant Pandya<sup>1</sup>



Hongjie Qian<sup>1</sup>

With:

A. Hsu<sup>2</sup>, C.-H. Wu<sup>4</sup>, D. Storaska<sup>5</sup>, D. Visani<sup>1</sup>, G. Wang<sup>1</sup>, J. Gu<sup>1</sup>, J. Zang<sup>1</sup>,  
M. Hasan<sup>1</sup>, M. H. Kashani<sup>2</sup>, M. Yang<sup>1</sup>, R. Ho<sup>7</sup>, S. Sivakumar<sup>1</sup>, S. Xu<sup>7</sup>,  
X. Yang<sup>1</sup>, X. Han<sup>1</sup>, Z. Guo<sup>1</sup>, Z. Yu<sup>1</sup>, Z. Yan<sup>1</sup>, H. Wang<sup>1</sup>, K. Chang<sup>1</sup>

<sup>1</sup>Marvell, Santa Clara, CA

<sup>2</sup>Marvell, Toronto, Canada

<sup>3</sup>Marvell, Bangalore, India

<sup>4</sup>Marvell, Zhubei, Taiwan

<sup>5</sup>Marvell, Fishkill, NY

<sup>6</sup>Marvell, Boston, MA

<sup>7</sup>Marvell, Burlington, VT

<sup>8</sup>Marvell, Kanata, Canada

Demonstration-Session Award

This Award is based on ratings provided by the Demo-Session Committee from the previous year.



Solid-State Circuits Society Awards

## 2024 Journal of Solid-State Circuits Best Paper Award

“8-λ × 50 Gbps/λ Heterogeneously Integrated Si-Ph DWDM Transmitter”



Cooper S. Levy



Zhe Xuan



Jahnvi Sharma



Duanni Huang



Ranjeet Kumar



Chaoxuan Ma



Songtao Liu



Jinyong Kim



Xinru Wu



Tolga Acikalin



Haisheng Rong



Ganesh Balamurugan



James E. Jaussi

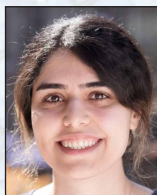
Intel Labs, Hillsboro, OR

## 2024 Journal of Solid-State Circuits Best Student Paper Award

“A Wireless, Multicolor Fluorescence Image Sensor Implant for Real-Time Monitoring in Cancer Therapy”



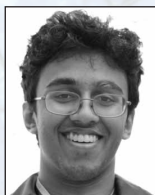
Micah Roschelle<sup>1</sup>



Rozhan Rabbani<sup>1</sup>



Surin Gweon<sup>1</sup>



Rohan Kumar<sup>1</sup>



Alec Verduyze<sup>1</sup>



Nam Woo Cho<sup>2</sup>



Matthew H. Spitzer<sup>2</sup>



Ali M. Niknejad<sup>1</sup>



Vladimir M. Stojanović<sup>1</sup>



Mekhail Anwar<sup>2</sup>

<sup>1</sup>University of California at Berkeley, Berkeley, CA, <sup>2</sup>University of California at San Francisco, San Francisco, CA

## 2025 Journal of Solid-State Circuits Test of Time Award

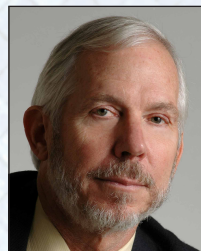
“Low-power CMOS digital design”



Anantha P. Chandrakasan



Sam Sheng



Robert W. Brodersen

University of California, Berkeley, CA

Established in 2024, the JSSC Test of Time Award is a biennial award honoring papers published at least a decade earlier that have demonstrated significant and sustained impact on the field of solid-state circuits.

## 2025 IEEE Solid-State Circuits Society Distinguished Service Award



Jeremy Cosson-Martin  
AMD, Toronto, Canada

This award recognizes exceptional and distinguished service to the IEEE Solid-State Circuits Society.



## Solid-State Circuits Society Awards

### 2025 IEEE Solid-State Circuits Society Industry Impact Award



**Taku Umebayashi**

*Sony Semiconductor Solutions, Kanagawa, Japan*

The IEEE SCS Industry Impact Award recognizes and honors SCS members who have had a seminal impact and made distinctive contributions to the field of solid-state circuits and the integrated circuits industry. The award recognizes extraordinary accomplishments in entrepreneurship, leadership and/or technical excellence that led to significant breakthroughs and paradigm shifts in the IC business, thereby enabling new products and services within the ten year period prior to the application deadline.

### 2025 IEEE Solid-State Circuits Society New Frontier Award



**Lin Cheng**

*University of Science and Technology of China, Hefei, China*

The IEEE SCS New Frontier award recognizes and honors SCS members in their early career who are exploring innovative and visionary technical work within the field of solid-state circuits. The award aims to pioneering developments that are at the frontiers of IC design or possess an imminent potential to expand the field through new categories of circuit technologies, system design, and/or emerging applications.

### 2025 IEEE Solid-State Circuits Society James D. Meindl Innovators Award



**Anthony Patrick Mauro**

*UCSD Futures and Extended Studies, San Diego, CA*

This award supports innovation in the field of solid-state circuits by funding projects that build excitement around the field among future generations, encouraging their participation, and awarding applicants whose project proposals are selected.

### 2025 IEEE SCS Predoctoral Achievement Prize



**Willy Sansen**



**Ziyuan Wen**

*Rice University, Houston, TX*

*in memory of Willy Sansen*

## 2025 IEEE SSCS Predoctoral Achievement Prize



*Kenneth C. (KC) Smith*



**Zihan Wu**

*Peking University, Beijing, China  
in memory of Kenneth C. (KC) Smith*

## 2025 IEEE SSCS Chapter of the Year Award

Lahore Section APS/CAS/MTT/SSCS Joint Chapter (Pakistan)



**Muhammad Qasim Mehmood**  
*Chair*



**Sadia Noureen**  
*Vice Chair*



**Sumbel Ijaz**  
*General Secretary*



**Noreen Raheem**  
*Treasurer*

## 2025 SSCS Student Branch Chapter of the Year Award

Alexandria University SSCS Student Branch Chapter (Egypt)



**Islam I. Abdulaal**  
*Chair*



**Alyedeen Hassan**  
*Vice Chair*



**Nour Karem**  
*Vice Chair*



**Ziad Mostafa**  
*Secretary-Treasury*



**Sayed Eid**  
*Chapter Advisor*



**Mai Banawan**  
*Branch Counselor*

## 2025 SSCS Student Branch Chapter of the Year Award

Saintgits College of Engineering-Kottayam SSCS Student Branch Chapter (India)



**S Govind**  
*Chair*



**Ajith Ravindran**  
*Advisor*



**Asher Jacob**  
*Secretary*



**Anna Boban**  
*Vice Chair*



**Adithyan B**  
*Treasurer*

## 2025 SSCS Chapter with Best Educational Program Award

Kolkata Section SSCS Chapter (India)



**Kalyan Biswas**  
*Chapter Chair*



**Joydeep Basu**  
*Chapter Secretary*



**Sudipta Ghosh**  
*Chapter Treasurer*



**Swarnil Roy**  
*Immediate Past Chair*



**Sagar Mukherjee**  
*Past Chair*



**Chandan Kumar Sarkar**  
*Past Chair*



## Solid-State Circuits Society Awards

### 2025 Chapter with Best Pre-University Outreach Award Central Illinois Section SSCS Chapter (USA)



**Dinup Sukumaran**  
Chair



**Marjorie Catt**  
Vice Chair

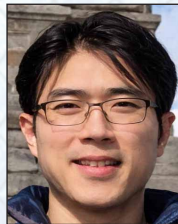
### 2025 Chapter with Best Pre-University Outreach Award San Diego APS/CAS/EDS/MTTS/SSCS Joint Chapter (USA)



**Jeff Shi**  
Chair



**Mohamed Abouzied**  
Vice Chair



**Albert Chou**  
Secretary/Webmaster



**Alvin Loke**  
Pre-University Outreach



**Hasnain Lakdawala**  
Industry Liaison



**Michelle Thompson**  
Section Advisor

### 2025 Wiley-IEEE Press Professional Book Award



**Bernhard Wicht**  
University of Hannover, Hannover, Germany

*"for his book Design of Power Management Integrated Circuits"*

### AIME 2026 John Fritz Medal



**Mau Chung Frank Chang**  
University of California, Los Angeles

*"For pioneering contributions and innovations in the development of heterojunction and system-on-chip technologies with unprecedented functionality, bandwidth and reconfigurability"*



## 2026 IEEE Donald O. Pederson Award in Solid-State Circuits

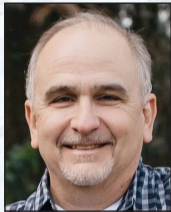


**Jan M. Rabaey**

University of California at Berkeley, Berkeley, CA

*"For seminal and visionary contributions to the VLSI implementation of low-energy signal processing and communication systems."*

The IEEE annually recognizes outstanding contributors worldwide to the art and science of electro- and information technologies with technical awards in 3 dozen technical specialties. The IEEE Donald O. Pederson Award for Solid-State Circuits for outstanding contributions in the field of solid-state circuits goes to an individual or team of not more than three. The award includes a bronze medal, certificate, and cash honorarium which are presented at the ISSCC.



**Thomas Burd**

Advanced Micro Devices, Santa Clara, CA  
*"for contributions to energy-efficient and high-performance microprocessor design"*



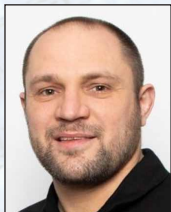
**Sudipto Chakraborty**

IBM T. J. Watson Research Center  
Yorktown Heights, NY  
*"for contributions to ultra-low power circuits and systems"*



**Debendra Das Sharma**  
Intel, Santa Clara, CA

*"for leadership in establishing PCI-Express, CXL, and UCIe industry-standards, fostering innovation in computing"*



**Anthony Ghiotto**

IMS Research Center, Bordeaux INP  
*"for contributions to substrate integrated waveguide technologies"*



**Jaydeep Kulkarni**

University of Texas at Austin, TX  
*"for contributions to low-power SRAM and compute-in-memory circuit technologies"*



**Antonio Liscidini**

University of Toronto, Toronto, Canada  
*"for contributions to circuit design for wireless communication radios"*



**Pedram Mohseni**

Case Western Reserve University, Cleveland, OH  
*"for contributions to biosensors and bioelectronics for mitigating debilitating blood- and brain-related disorders"*



**Yusuke Oike**

Sony Semiconductor Solutions, Kanagawa, Japan  
*"for contributions to CMOS image sensors with stacked device technologies"*



**Ada Poon**

Stanford University, Stanford, CA  
*"for contributions to wireless power transfer and implantable devices"*



# IEEE

## 2026 Fellows



**Jafar Savoj**  
Apple, Cupertino, CA

*"for contributions to the design of high-speed wireline transceiver circuits and systems"*



**Shahriar Shahramian**

Bell Laboratories, Nokia, Murray Hill, NJ

*"for the development of millimeter-wave phased-arrays and D-band communication links"*



**Farhana Sheikh**  
Intel, Hillsboro, OR

*"for contributions to digital signal processing and 2.5D/3D heterogeneous integration"*



**Lawrence Tse**

LTLT Consulting, LLC, Fremont, CA

*"for leadership in design and commercialization of wireless and wireline data communication systems"*



**Leo de Vreede**

Delft University of Technology, Delft, The Netherlands

*"for contributions to mixed-signal high frequency device characterization and digitally assisted wireless transmitters"*



**Huaqiang Wu**

Tsinghua University, Beijing, China

*"for contributions to resistive random-access memory and compute-in-memory technologies"*



**Geoffrey Choh-Fei Yeap**  
TSMC, Hsinchu, Taiwan

*"for leadership and contribution in development of advanced energy-efficient logic technology"*



**Jerald Yoo**

Seoul National University, Seoul, Korea

*"for contribution to Biomedical Circuits and Systems and Body-Area Network"*



**Mohammad Hossein Zarifi**

University of British Columbia, Vancouver, Canada

*"for contributions to applied electromagnetics and advanced materials for cutting-edge sensing and communication devices"*



**Zhengya Zhang**

University of Michigan, Ann Arbor, MI

*"for contributions to the design of high-performance and energy-efficient digital signal processors"*

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